



## STATE REGULATION OF PUBLIC UTILITIES REVIEW COMMITTEE

### CANDIDATES FOR THE SOUTH CAROLINA PUBLIC SERVICE COMMISSION EDITED PERSONAL DATA QUESTIONNAIRE

**NOTICE:** The information requested herein is needed to assist the Committee in its investigation of candidates for the Public Service Commission. This questionnaire is the initial step in the investigation of your candidacy. A completed questionnaire should be returned to the Committee as soon as possible, but in any event no later than noon, 12:00 p.m., on Friday, October 11, 2019. (Use and attach separate sheets, if necessary.)

**PLEASE SPECIFY THE SEAT FOR WHICH YOU ARE APPLYING (THE CONGRESSIONAL DISTRICT IN WHICH YOU RESIDE; YOU MAY ONLY APPLY FOR ONE SEAT):**

*Seat 5*

1. Full Name: *Mr. Luther Preston Hendrix*  
  
Home Address: *1877 Hasty Road  
Camden, SC 29020*  
  
Business Address: *1877 Hasty Road  
Camden, SC 29020*
6. List each institution of higher learning you attended, including dates of attendance and degrees awarded. Please provide an official transcript prior to your screening hearing. If you left any institution without receiving a degree, state the reason for leaving.  
*University of South Carolina, August 1973  
Bachelor of Science in Engineering (Electrical), Minor in Philosophy, May 14, 1977,  
Magna Cum Laude*  
  
*The Georgia Institute of Technology, June 1977  
Master of Science in Electrical Engineering (Physical Electronics), June 10, 1978,  
President's Fellow*
7. List any continuing education courses or other professional education or training programs that you have attended in the past five years.  
*None*
8. Have you been employed or held any position in any of the following areas?  
  
☐ Y (a) energy issues  
☐ Y (b) telecommunications issues

- ☐ N (c) consumer protection and advocacy issues  
☒ Y (d) water and wastewater issues  
☒ Y (e) finance, economics, and statistics  
☒ Y (f) accounting  
☒ Y (g) engineering  
☒ Y (h) law (ie. Expert Witness work, patents)

Please provide the duration of your employment or service and details about the nature of the work or the position.

*Founder and Consultant, Sabal Technology & Management Consulting*  
*Providing consulting services in most areas of Electrical Engineering, the management of those businesses, advice and due diligence for investors, and expert witness work for associated law firms.*  
*April, 2013 to Present*

9. Have you served in the military? If so, give the dates of service, branch of service, highest rank, serial number, present status, and the character of your discharge or release. Please provide a copy of your DD-214 prior to your screening hearing.  
*No*
10. Have you ever held public office? If so, provide details, including the office, whether elected or appointed, the length and dates of your service. Also, state whether or not you timely filed reports with the State Ethics Commission during the period you held public office. If not, were you ever subject to a penalty. If so, provide details, including dates.  
*No*
11. Have you ever been an unsuccessful candidate for elective, judicial, or other public office? If so, provide details, including dates.  
*No*
12. Please list in chronological order any employment of you by any governmental agency (whether full time or part time, contractual or at will, consulting or otherwise). Provide dates of employment, name of employer, name of supervisor, and major job responsibilities.  
*No direct employment, multiple short-term consulting projects.*  
*Direct employment at Charleston Naval Shipyard, Electronics Apprentice, August 1971 to August 1972*
13. Please list in chronological order any occupation, business, or profession in which you have been engaged or employed (other than serving in a public office or being employed by a governmental agency). Please briefly note the nature of your work during each period.

**4/13-Current Sabal Technology & Management Consulting**

**President Austin, TX & Camden, SC**

Founder of a consulting services startup, specializing in electronics and semiconductor products and processes.

**4/09-4/13 Kevin Kennedy Associates**

**Senior Lead Consultant Indianapolis, IN**

Served as both Consultant and Project Manager for a wide variety of technical, legal, and investment clients. Projects included semiconductors, utility power, solar power, energy harvesting, machine tools, SPC, quality systems, pump seals, floating docks, and others, Managed client relations, created proposals and closed sales.

**8/05-4/09 Omnibase Logic, Inc.****President & CEO Austin, TX**

I led a multinational startup venture to commercialize the industry's first practical method of implementing multiple-valued logic functionality in standard CMOS process and design systems,

**10/04-8/05 Sabal Semiconductor, Inc.****President Austin, TX**

I formed and led a new startup venture to produce an innovative highly-integrated power supply design for the implementation and control of high-brightness LED's, This product enabled the use of LED's for the direct replacement of standard incandescent and halogen bulbs in commercial, industrial and household implementations, We produced the initial product design and working prototypes,

**10/03-10/04****Independent Consultant Austin, TX**

I provided consulting services on yield problems, device and process design, chip production economics, DRAM and module resale, wafer fab data analysis, and parametric test equipment design, I also provided consulting services to the legal and investment community customers.

**12/01-10/03 Applied Materials****Consulting Services Manager Austin, TX**

I was responsible for the delivery of consulting services for the Yield Enhancement Services group, These services were based on proprietary data mining and analysis software and were used to identify and resolve yield, control and quality problems relating to products produced in the customers' wafer fabs,

**8/00-12/01 Global Knowledge Services****Director of Consulting Services Austin, TX**

I was one of the founding partners of a new startup selling technology consulting services to wafer fab customers, I hired and managed the consulting staff and developed the service offerings, We supported tool development, account management, customer relations, and sales support. My group and I performed analyses of customer data for yield and quality improvement,

Customers ranged from contact-print discrete lines to 130nm copper CMOS technology and were primarily Fortune 500 companies, GKS was acquired by Applied Materials.

**3/97- 8/00 Samsung Austin Semiconductor****Director of Product Technology Austin, TX**

I was responsible for Product Engineering, Process Architecture (integration), Device Engineering, Test Engineering, Test Maintenance and Test Manufacturing, I was one of the very early management hires and built all of these groups from scratch.

This was a state-of-the-art 200mm DRAM wafer fab startup, The initial process was set up for a 350nm 64M DRAM process, I led this organization through four generations of technology and to best-in-the-world yields and AQL's on a 230nm 128M process, My division had primary responsibility for the technology transfer, process setup and product qualification including yield, performance, and quality metric ownership,

I also served as Chairman of the Process Change Control Board, This board drove the setup of the initial spec systems and approved all initial specs, It controlled all process changes, test changes, and any technical spec change and all experimentation,

Additionally, I served on the HR, Policy, Patent and Philanthropy committees.

**9/93-2/97 National Semiconductor Corp. Sr. Manager of Technology Development Arlington, TX**

I built a new development group created by a strategic change to a distributed development model, I staffed, organized and directed this organization to support a two-fab facility with process technologies including BiCMOS, DMOS, and CMOS mixed-signal, along with CMOS logic with embedded SRAM, and NVM technologies to 0.5um with advanced device structures and multilevel interconnects, This included Process Integration, Device Engineering, Circuit, Process and Device modeling, DFM, and Unit Process development activities, We also

managed the transfer of technology into our fabs and out of our fabs to other National fabs.

**5/92-9/93: National Semiconductor Corp.**

**Engineering Manager Arlington, TX**

I was responsible for engineering activities in a startup submicron class-1 production fab, Responsibilities included Device Engineering, parametric test, SPC, yield modeling, yield enhancement, and CFM, We delivered an ahead-of-schedule fab startup with full device, process and fab qualification on first runs as well as yield on the first lot, wafer and die tested.

**1/91-5/92: Motorola**

**Principal Device Scientist Semiconductor Products Sector RISC Microprocessor Design , Austin, TX**

I did the SPICE models and device characterization, design rule optimization, and the integration of the process development and circuit design efforts in support of the development of advanced RISC-based microprocessor products, I led the design of the process development and modeling test chips, I also served as the Principal Device Engineer for the Power PC transfer and completed the generation of the initial SPICE model files used by Motorola, IBM, and Somerset.

**1/88-1/91:**

**Principal Staff Engineer Motorola DRAM Technology Dept, Semiconductor Products Sector MOS Memory Products Division Austin, TX**

I was responsible for the Device Engineering activities to support the transfer of Toshiba 1M and 4M DRAM technology into production in Motorola and joint venture wafer fabs, We analyzed wafers, product and data from all fabs and compared the performance of the fabs to ensure that the process was matched and that the product was consistent from all wafer fabs, This included modeling, parametric test, failure analysis, and process characterization.

We also supported other Motorola product and development groups with modeling, and device test and characterization, We provided engineering support for fab startup and production problems at all facilities including qual fail issues, yield issues and process improvements,

**6/86-1/88:**

**Principal Device Engineer Advanced Micro Devices Bipolar Technology Development San Antonio, TX**

I did Process and Device modeling and simulation in support of the development of advanced Bipolar processes, I developed models for dual-poly-emitter Bipolar devices from process definition through SPICE model decks, resulting in circuit performance modeling to within 10% of the initial Silicon, I continued this function through the technology exchange joint venture with Sony Corporation who subsequently bought the venture.

**1/84-6/86:**

**Process Development Engineering Manager Advanced Micro Devices Fab X Austin, TX**

I was head of the engineering department responsible for the Device Engineering, Process Integration and yield enhancement functions, We developed new process technologies including the 64K DRAM, nMOS SRAM and a triple-poly (1.0 um) 16K FSRAM.

My group created and brought into production the process to produce the second-source Intel 80186 and 80286 processors with superior performance, costs and yields versus the Intel process, We were accountable for wafer sort, laser, parametric test, and yields in a then state-of-the-art 5" MOS wafer fab.

**2/81-1/84: Mostek**

**Device Engineering Supervisor Process R&D Modeling Department Carrollton, TX**

I lead the group that was responsible for the characterization of process experiments in support of the development of 64K, 256K and 1Mbit DRAM process technologies.

I was one of the principal contributors to the development of the process test vehicles and directed the layout of those designs, We designed the process verification structures, the test structures for the verification of design rule limitations, and defect arrays.

I was one of the primary authors of the Design Rule Manual and the principal author of the Layout Design Rules and the modeling decks.

I directed efforts toward HCE testing and transistor and interconnect reliability screening for process R&D.

We supported Process R&D with modeling using SUPREM, SEDAN, Minimos and other tools.

I developed the circuit models for the 64K cell/bitline structures and improved Mostek's proprietary SPICE model equations.

1/79-2/81:

**Bipolar Product Engineering Manager Data General Corp. Semiconductor Division Sunnyvale, CA**

I was responsible for process integration, device engineering, parametric testing, and new product introduction for bipolar product lines of >150 products, Process technologies ranged from diode arrays to bit-slice microprocessors and PALs, We produced every chip in the Nova and Eclipse computers.

Author of the Composite Schottky Design Rules used for PALs, PROMs and uP's as well as the ECL design rules for gate array projects, I designed the process development test vehicles and supported the process development efforts for all processes.

6/78-1/79: **Monolithic Memories, Inc.**

**Product Engineer Sunnyvale, CA**

I was responsible for the 8M family of Bipolar PROM's, PAL's, and the introduction of a new line of interface chips, These responsibilities were assumed on day one on the job, I also helped debug and fix several process problems in support of the startup of a new 4" wafer fab.

*Prior to college:*

*I worked as a mechanic at Langston Motors, Summerville, SC, 6/71 to 8/71*

*Electronics Apprentice, Charleston Naval Shipyard, 8/71 to 6/72*

*Electricians Helper, Daniel Construction, 6/72 to 8/72*

*Installer/lineman, Southern Bell, Charleston, 8/72 to 8/73.*

14. Are you now an officer or director or involved in the management of any business enterprise? Explain.

*Managing Director, Sabal Technology & Management Consulting*

*Current consulting business*

*Managing Director, StarBright Solar*

*Solar installation enterprise*

*Founder, 1000 Suns Energy*

*Solar equipment development*

*Director, MVLock*

*Non-binary circuit intellectual property development*

15. Describe any financial arrangements or business relationships that you currently have or have had in the past that could pose a conflict of interest in the position you seek. Explain how you would resolve any potential conflict of interest.

*StarBright Solar does installation, repair, and consulting for residential solar power systems. I would have to divest and close operations since decisions by the Commission could affect that business.*

20. Have you ever been a named party in a lawsuit, either personally or professionally? If so, provide details.

*Yes, sued for fraud on a home sale where flooding was not disclosed as there had been none, but the pool at the home was flooded during development of the woods behind the house.*

22. Are you now or have you ever been employed as a "lobbyist," as defined by S.C. Code Section 2-17-10(13), or have you acted in the capacity of a "lobbyist's principal," as defined by S.C. Code Section 2-17-10(14)? If so, please provide the dates of your

employment or activity in such capacity and specify by whom you were directed or employed.

*No*

23. Since your decision to seek a position on the Public Service Commission, have you accepted lodging, transportation, entertainment, food, meals, beverages, money, or any other thing of value, as defined by S.C. Code Section 2-17-10(1), from a lobbyist or lobbyist's principal? If so, please specify the item(s) received, date of receipt, and the lobbyist or lobbyist's principal providing the item(s).

*No*

26. Other than expenditures for travel or room and board, please itemize all expenditures (by amount, type, and date) by you, or on your behalf, in seeking the office of Public Service Commissioner.

*\$25 for copy of transcripts from USC*

*Mailing costs for Letters of Recommendation by the authors (unknown amounts)*

27. Please list the amount and recipient of all contributions made to members of the General Assembly within six months of filing this questionnaire.

*None*

28. Have you directly or indirectly requested a pledge of any member of the General Assembly as to your election for the position for which you are being screened?

*No*

Have you received the assurance of any public official or public employee that they will seek the pledge of any member of the General Assembly as to your election for the position for which you are being screened?

*No*

29. Have you or has anyone on your behalf solicited or collected funds to aid in the promotion of your candidacy for the position of Public Service Commissioner? If so, please specify the amount, solicitor (if applicable), donor, and date of solicitation and/or receipt.

*No*

30. List all professional organizations of which you are a member and give the titles and dates of any offices that you have held in such groups.

*The Institute of Electrical and Electronics Engineers (IEEE), Senior Member*

31. List all civic, charitable, religious, educational, social, and fraternal organizations of which you are or have been a member during the past five (5) years.

*Springdale Hall Club, Camden, SC*

*Gamecock Club, Secretary of Kershaw County Chapter*

*University of South Carolina Alumni Association, Life Member*

*Georgia Institute of Technology Alumni Association*

32. State any other information which may reflect positively or negatively on you, or which you believe should be disclosed in connection with consideration of you for a position on the Public Service Commission.

*None*

33. List the names, addresses, and telephone numbers of five (5) persons, including your banker, from whom references could be required. Also, provide this Committee with original letters of recommendation from each person listed herein, including their signature. A photocopy, facsimile, or electronic submission of a letter of recommendation will not be accepted.

1) Banker Letter

2) David Baker

3) Paul Proctor

4) Bill Godbold

5) Roy Guild

YOUR SIGNATURE WILL BE HELD TO CONSTITUTE A WAIVER OF THE CONFIDENTIALITY OF ANY PROCEEDING BEFORE A GRIEVANCE COMMITTEE OR ANY RECORD INFORMATION CONCERNING YOUR CREDIT.

I HEREBY CERTIFY THAT THE ANSWERS TO THE ABOVE QUESTIONS ARE TRUE AND CORRECT TO THE BEST OF MY KNOWLEDGE.

Signature:

Date:

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***PLEASE NOTE: S.C. CODE SECTIONS REFERRED TO IN THIS DOCUMENT ARE CONTAINED IN THE PDQ CODE REFERENCES DOCUMENT.***

**I ACKNOWLEDGE, AS A CANDIDATE, THAT I AM RESPONSIBLE FOR REVIEWING THE FOLLOWING DOCUMENTS. THESE DOCUMENTS ARE PROVIDED ON THE USB FLASH DRIVE WITH THE PUBLIC SERVICE**

**COMMISSION APPLICATION MATERIALS AND ARE ACCESSIBLE ON THE  
PUBLIC UTILITY REVIEW COMMITTEE'S WEBSITE:**

- (1) An Introduction to the Public Service Commission;**
- (2) The press release issued by the Committee announcing the start of its application process;**
- (3) An Advisory Opinion relating to attendance by candidates for the Public Service Commission at legislative receptions;**
- (4) An Advisory Opinion relating to attendance by candidates for the Public Service Commission at legislative caucus meetings;**
- (5) The Judicial Code of Conduct;**
- (6) Statutory definitions and statute concerning pledges; and**
- (7) Information regarding accessing your credit report.**

**Signature:**

**Date:**